#### 900V N-Channel MOSFET

## Description

Silicon Carbide (SiC) MOSFET use a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size.

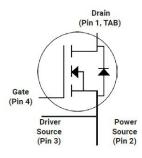
#### **Features**

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low RDS(on)
- Simple to drive with Standard Gate Drive
- 100% avalanche tested
- Maximum junction temperature of 150°C
- ROHS Compliant

## **Application**

- EV Charging
- DC-AC Inverters
- High Voltage DC/DC Converters
- Switch Mode Power Supplies
- Power Factor Correction Modules
- Motor Drives





## **Ordering Information**

Part Number	Marking	Package	Packaging
JX4S0020090M	JX4S0020090M	TO-247	Tube



# Absolute Maximum Ratings(Tc=25℃)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	900	V
I <sub>D</sub>	Drain Current(continuous)at Tc=25℃	100	А
I <sub>D</sub>	Drain Current(continuous)at Tc=100°C	70	А
I <sub>DM</sub>	Drain Current (pulsed)	200	А
V <sub>GS</sub>	Gate-Source Voltage	-10/+20	V
P <sub>D</sub>	Power Dissipation T <sub>C</sub> = 25°C	425	W
T <sub>J</sub> , Tstg	Junction and Storage Temperature Range	-55 to +150	°C

# Electrical Characteristics( $T_J = 25^{\circ}C$ unless otherwise specified)

# **Typical Performance-Static**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
BV <sub>DS</sub>	Drain-source Breakdown	In=250uA.V <sub>GS</sub> =0V	900			V
<b>DV</b> DS	Voltage	1D-250uA, VGS-0V	900			V
I <sub>DSS</sub>	Zero Gate Voltage Drain	V <sub>DS</sub> =900V, V <sub>GS</sub> =0V,		100	uA	
IDSS	Current	TJ=25°C			100	uA
I <sub>GSS</sub>	Gate-body Leakage Current	V <sub>DS</sub> =0V ; V <sub>GS</sub> =10 to 20V			250	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =15mA	2		4	V
D	Static Drain-source On	V <sub>GS</sub> =20V. I <sub>D</sub> =50A		16	22	mΩ
R <sub>DS(on)</sub>	Resistance	VGS-ZOV, ID-SOA		10		11122
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V,f=1MHz		3		Ω

## **Typical Performance-Dynamic**

C <sub>iss</sub>	Input Capacitance		4910	pF
Coss	Output Capacitance	V <sub>DS</sub> =600V,f=1000KHz,V <sub>GS</sub> =0V	129	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		27	pF
Qg	Total Gate Charge	V <sub>DS</sub> =600V, I <sub>D</sub> =50A,V <sub>GS</sub> =-4~20V	167	nC
Q <sub>gs</sub>	Gate-source Charge		58	nC
$Q_{gd}$	Gate-Drain Charge		47	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =600V,ID=50A, V <sub>GS</sub> =-4V~20V.	157	ns
t <sub>r</sub>	Rise Time		28	ns
t <sub>d(off)</sub>	Turn-off Delay Time	$R_{G}=0\Omega$	81	ns
t <sub>f</sub>	Fall Time	- ,	27	ns





## **Typical Performance-Reverse Diode**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V	Forward Voltage	V <sub>GS</sub> =0V,I <sub>F</sub> =30A,T <sub>J</sub> =25°C	3		6	V
V <sub>FSD</sub>	Forward Vollage	V <sub>GS</sub> =0V,I <sub>F</sub> =30A,T <sub>J</sub> =150°C	3		6	V
trr	Reverse Recovery Time	V <sub>GS</sub> =0 V, I <sub>F</sub> =30 A, V <sub>R</sub> =600 V, d <i>i</i> /d <i>t</i> = 100 A/μs		86		ns
Qrr	Reverse Recovery Charge			876		nC
	Peak Reverse Recovery			20		А
Irrm	Current	αναι - 100 Ανμ3		20		A

## **Thermal Characteristics**

Symbol	Parameter	Value.	Unit
Rejc	Thermal Resistance, Junction-to-Case	0.3	°C/W
Reja	Thermal Resistance, Junction-to-Case	40	°C/W

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of Tj(max)=150 $^{\circ}$ C



## •Electrical characteristic curves

Fig.1 Typical Output Characteristics(I)

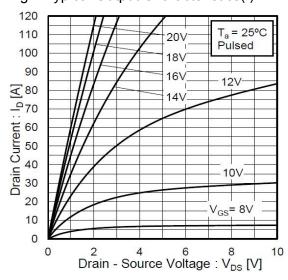


Fig.2 Typical Output Characteristics(II)

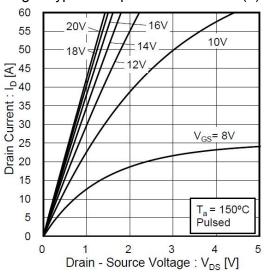


Figure 3. Normalized On-Resistance vs. Tj

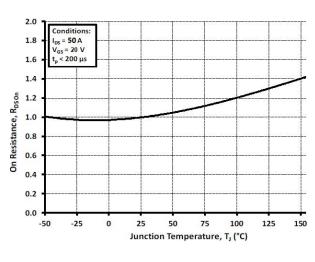


Figure 4. On-Resistance vs. Drain

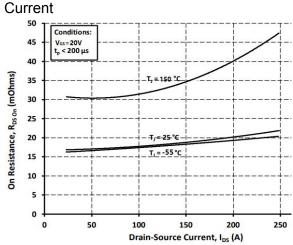
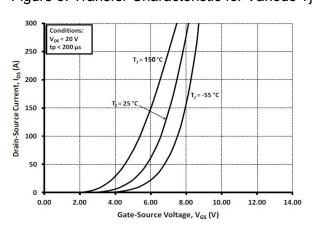
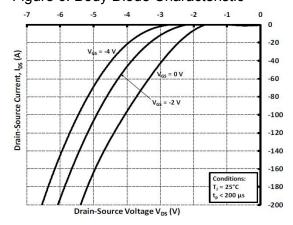


Figure 5. Transfer Characteristic for Various Tj Figure 6. Body Diode Characteristic







## JX4S0020090M

Figure 7. Threshold Voltage vs. Tj

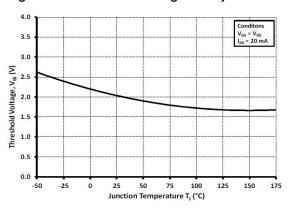


Figure 8. Gate Charge Characteristics

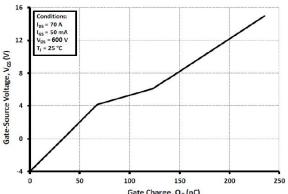


Figure 9. Output Capacitor Stored Energy

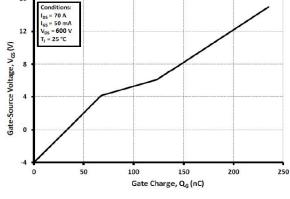
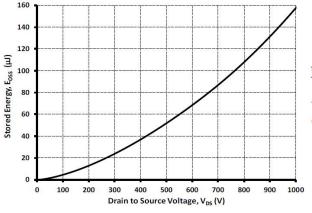


Figure 10. Capacitances vs. VDS



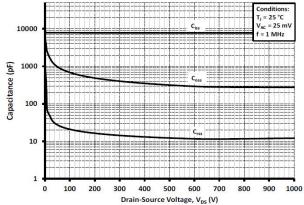


Figure 11. Continuous Drain Current vs. Tc

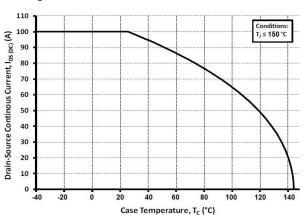
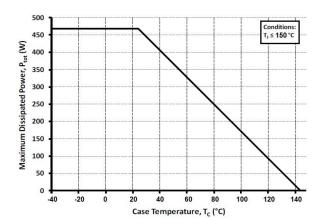


Figure 12. Maximum Power vs. Tc

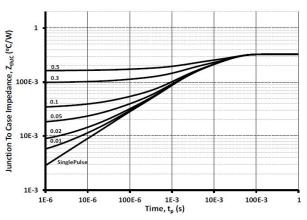




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Figure 13. Transient Thermal Impedance

Figure 14. Safe Operating Area



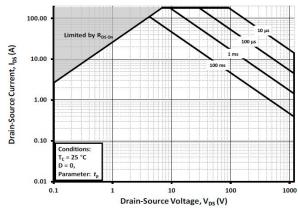
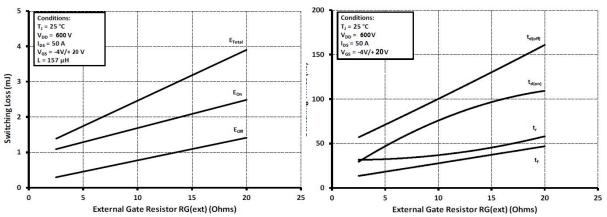


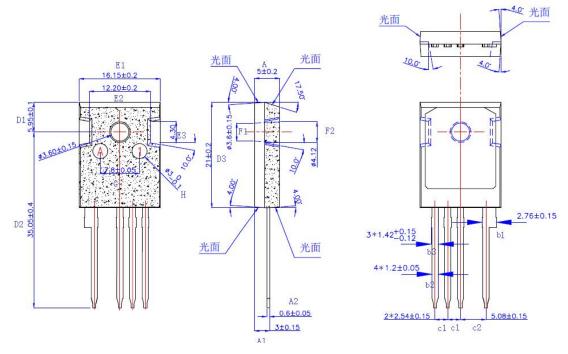
Figure 15. Switching Energy vs. RG(ext)

Figure 16. Switching Times vs. RG(ext)





## Package Drawing:



Dimensions ( UNIT: mm)

SYM	MILLIMETERS		SYM	MILLIMETERS	
	MIN	MAX		MIN	MAX
A	4.98	5.02	D2	34.65	35.45
A1	2.85	3.15	D3	20.80	21.20
A2	0.55	0.65	E1	15.95	16.35
b1	2.61	2.91	E2	12.00	12.40
b2	1.15	1.25	F1	3.45	3.75
b3	1.30	1.57	F2	4.12	4.12
c1	2.39	2.69	G	7.75	7.85
c2	4.93	5.23	Н	2.90	3.10
D1	5.85	6.05			